

PATENT

IN THE UNITED STATES PATENT OFFICE

Applicants: Amit Dhir, Krishna Rangasayee
Assignee: Xilinx, Inc.
Title: Programmable Logic Device for Wireless Local Area Network
Serial No.: 10/007,841 File Date: 12-03-01
Examiner: Jonathan Liou Art Unit: 2663
Docket No.: X-881 US Conf. No.: 7622

Mail Stop AMENDMENT
COMMISSIONER FOR PATENTS
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Alexandria, Virginia 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicants bring to the attention of the Examiner the two (2) references listed in the attached Substitute for Form 1449A/PTO.

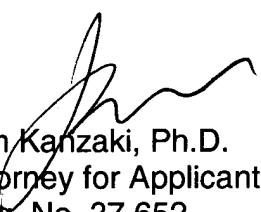
The enclosed references are being cited after the receipt of a first office action after the filing of an RCE but prior to a final action after the RCE.

Citation of the above documents shall not be construed as an admission that the documents are necessarily prior art with respect to the instant invention. Citation of the above documents shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above documents shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

The Commissioner is hereby authorized to charge the fee of \$180.00 due under 1.17(p) and any additional fees, or credit any overpayment which may be required to Deposit Account No. 24-0040.

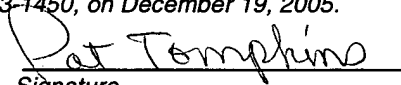
Respectfully submitted,

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*I hereby certify that this correspondence is being deposited with the United States Postal Service as **first class mail** in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450, on December 19, 2005.*

Pat Tompkins
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